

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of :  
:   
Hiroshi MATSUUCHI, et al. :  
:   
Serial No.: : Group Art Unit:  
:   
Filed: April 04, 2001 : Examiner:  
:   
For: DIGITAL DEVICE, DATA INPUT-OUTPUT CONTROL METHOD, AND DATA  
INPUT-OUTPUT CONTROL SYSTEM

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, DC 20231

Sir:

Prior to examination of the above-referenced application, please amend the application as follows:

**IN THE CLAIMS:**

Please amend claim 4 and claim 5 as follows:

4. (Amended) A digital device according to [either one of] claim 1[ to 3], wherein the data is synchronous stream one.

5. (Amended) A digital device according to [either one of] claim 1[ to 3], wherein the bus interface is compliant with the standard of IEEE1394.

Please add new claims 13 through 16 as follows:

--13. A digital device according to claim 2, wherein the data is synchronous stream one.

14. A digital device according to claim 3, wherein the data is synchronous stream one.
15. A digital device according to claim 2, wherein the bus interface is compliant with the standard of IEEE1394.
16. A digital device according to claim 3, wherein the bus interface is compliant with the standard of IEEE1394.--

#### REMARKS

The above preliminary amendment is being filed to delete the multiple dependency of claims 4 and 5. A clean copy of claims 4 and 5 as amended is attached herewith. Entry of this preliminary amendment is respectfully requested.

Respectfully submitted,

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#### **Claims 4 and 5 As Amended**

4. A digital device according to claim 1, wherein the data is synchronous stream one.
5. A digital device according to claim 1, wherein the bus interface is compliant with the standard of IEEE1394.